

FIG. 1

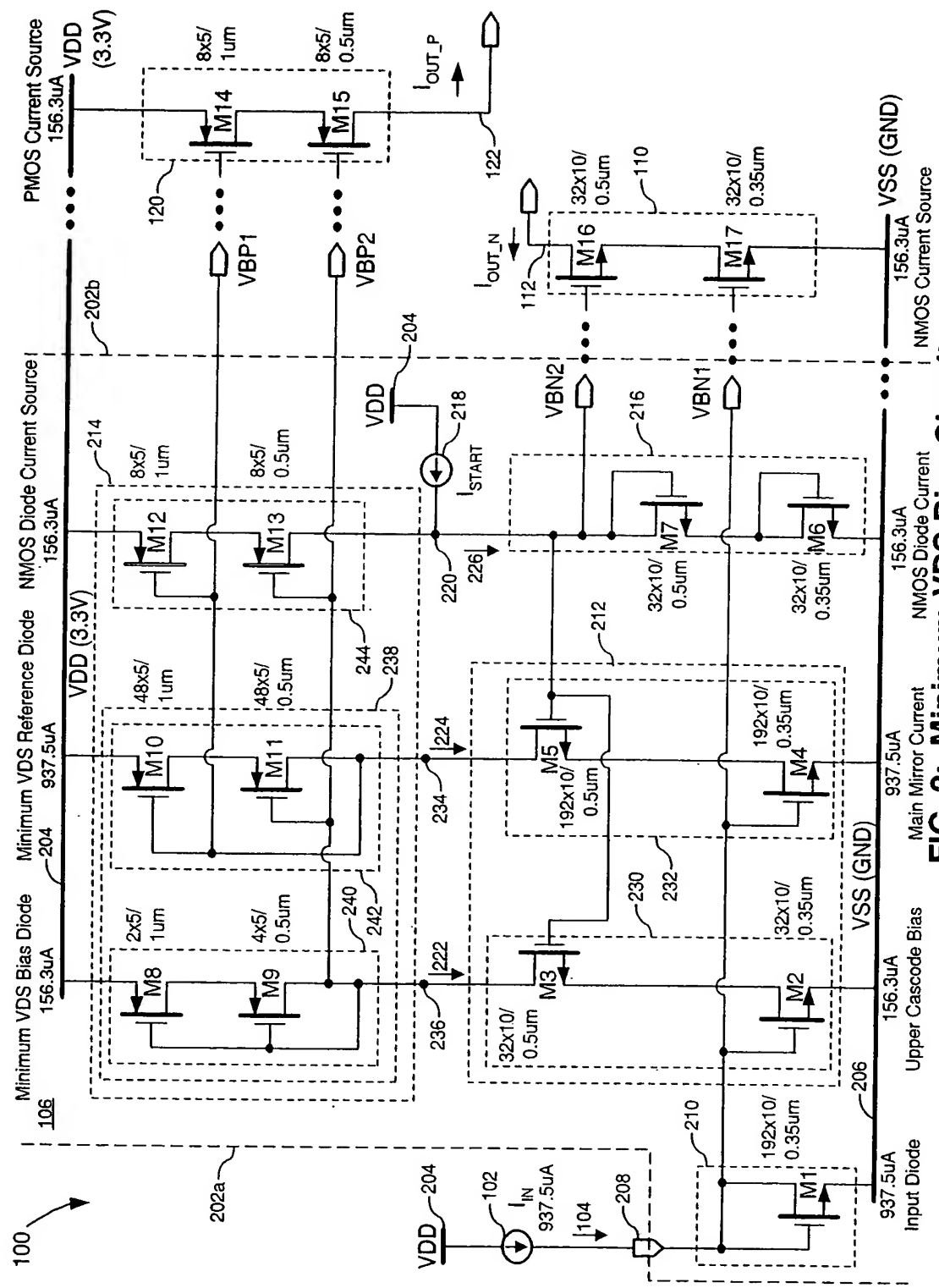


FIG. 2: Minimum VDS Bias Circuit

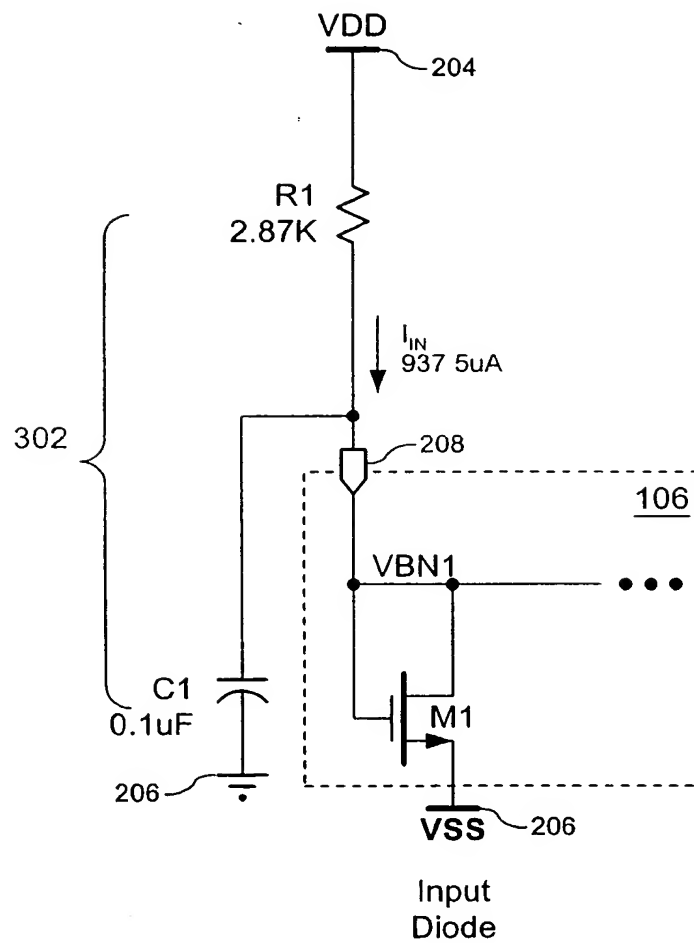


FIG. 3:
Using A Resistor To Supply Input Bias Current

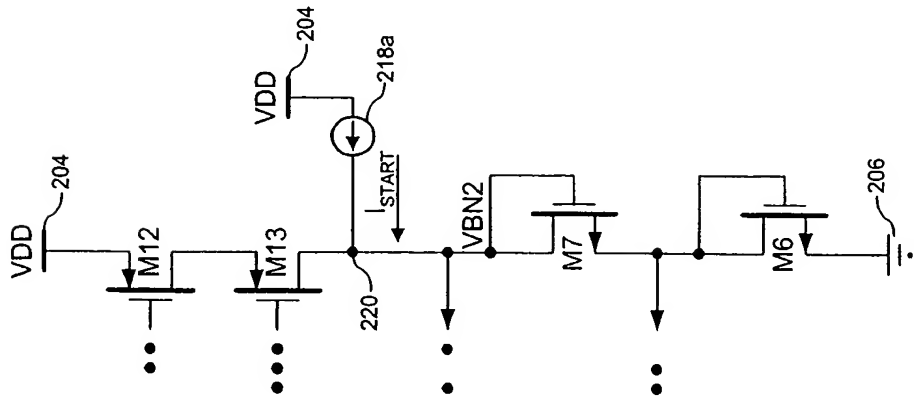


FIG. 4A

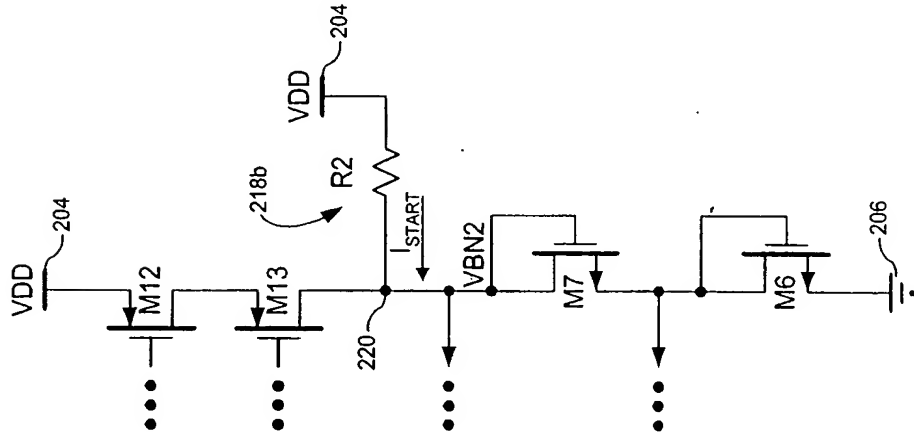


FIG. 4B

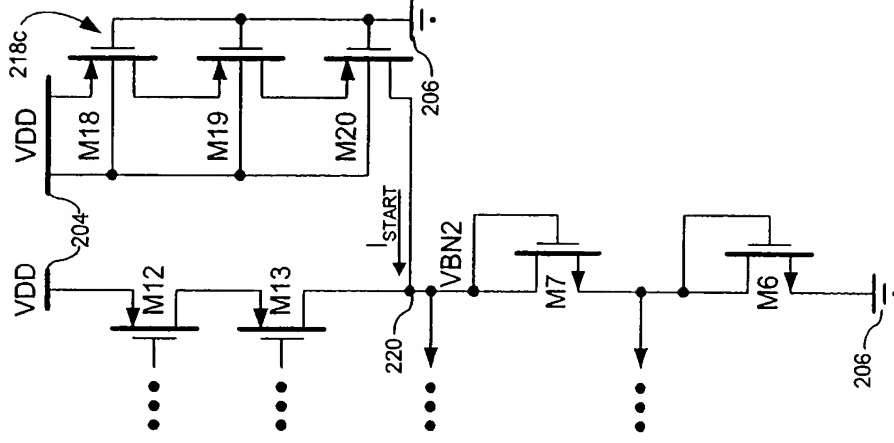


FIG. 4C

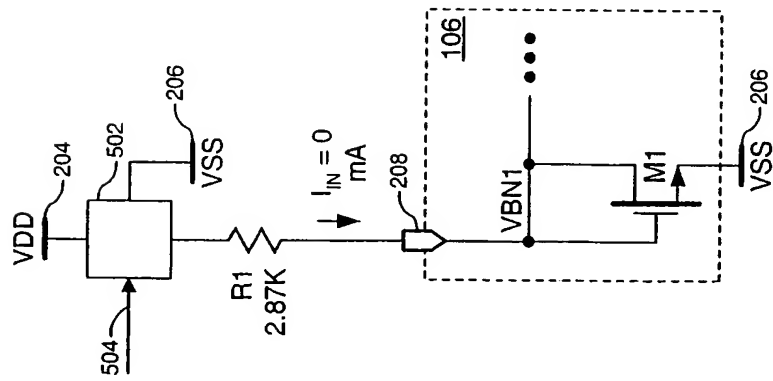


FIG. 5A

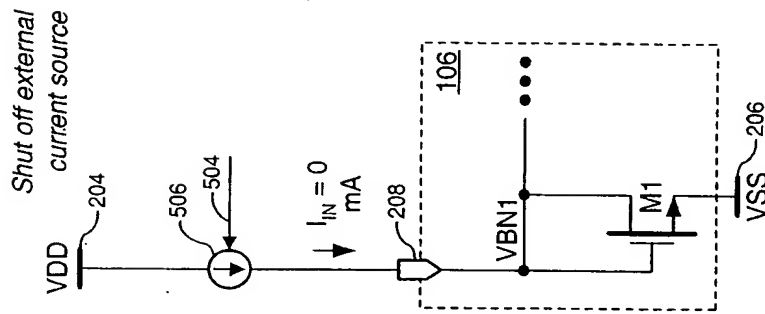


FIG. 5B

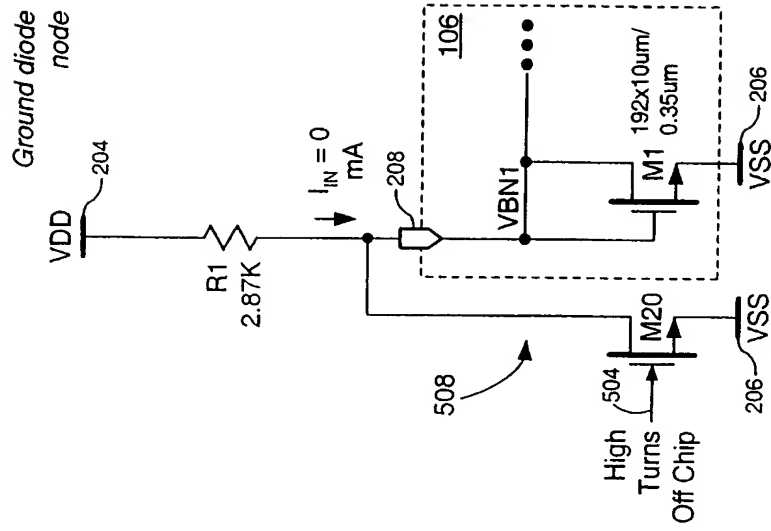


FIG. 5C

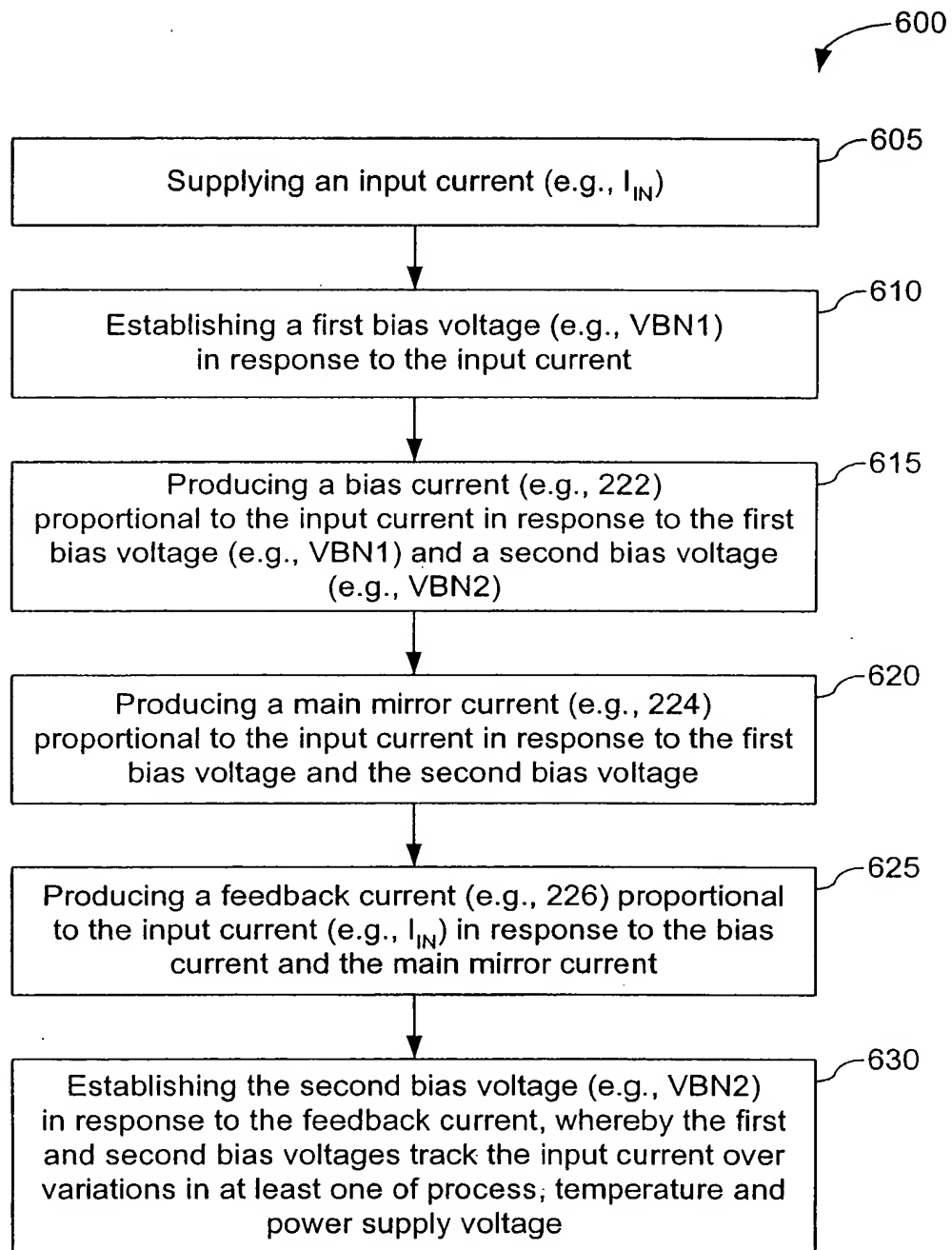


FIG. 6A

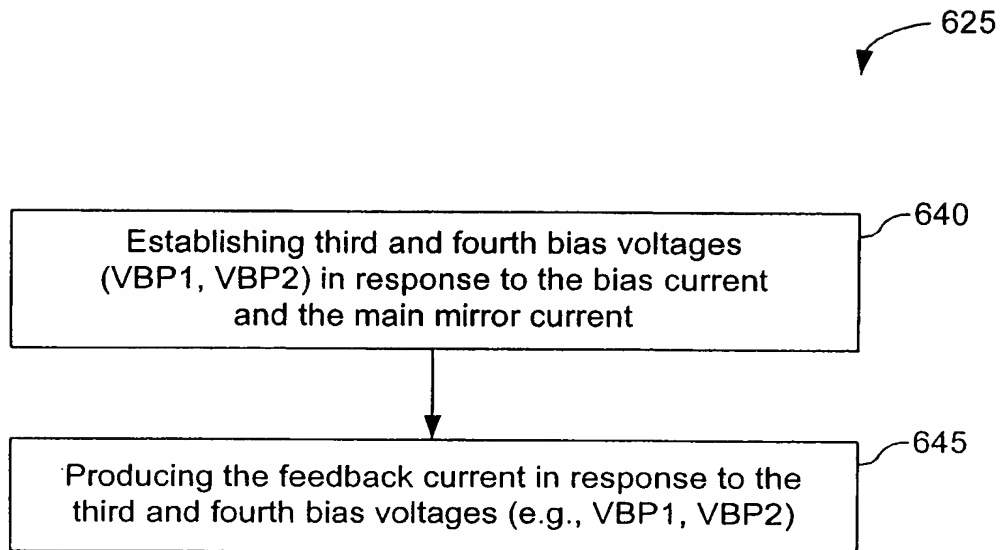


FIG. 6B

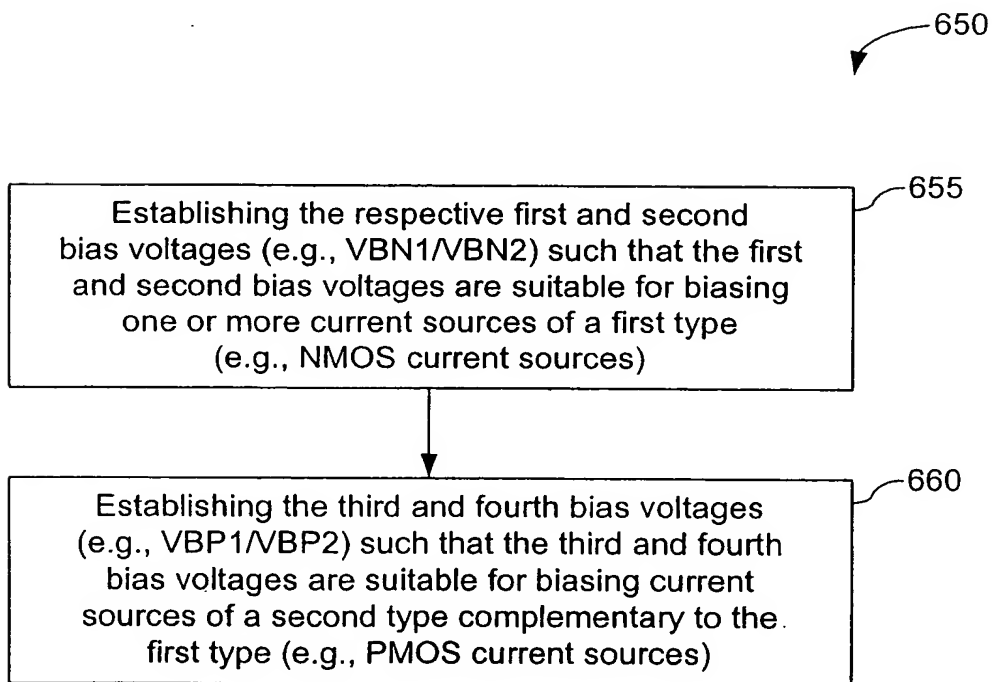


FIG. 6C

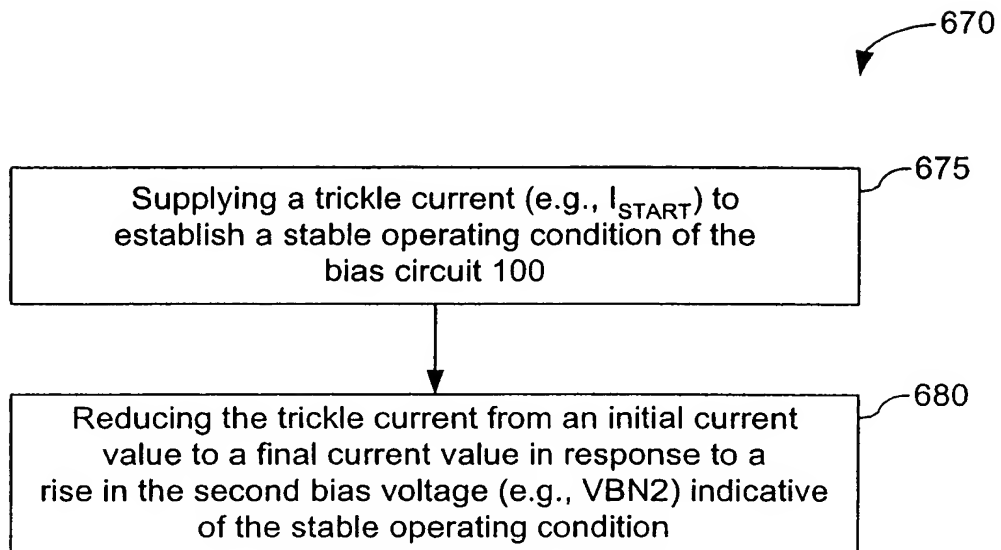


FIG. 6D

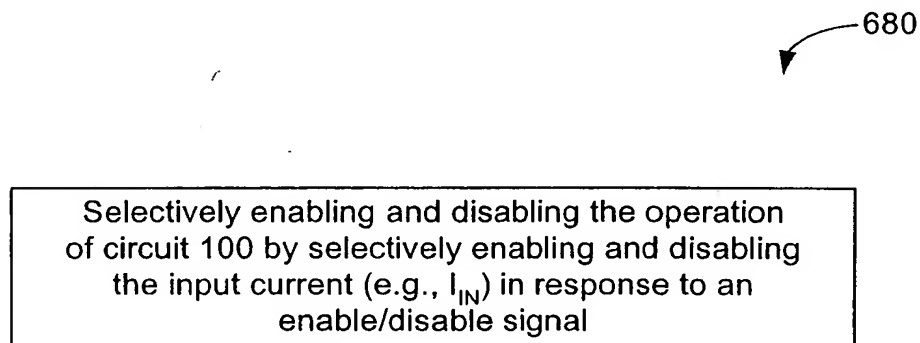
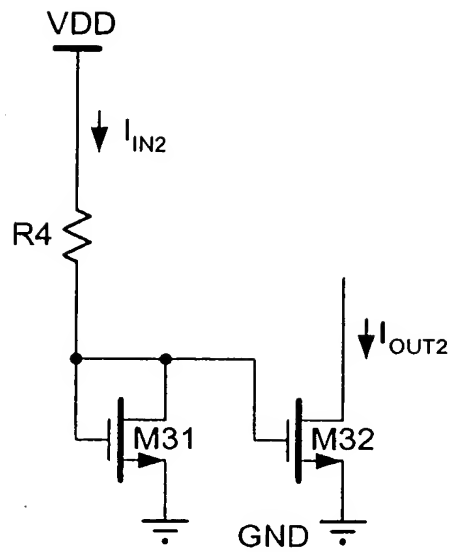
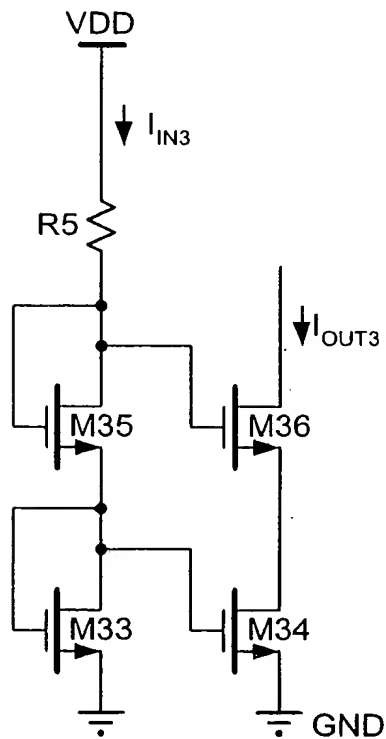


FIG. 6E



Simple Current Mirror
FIG. 7A (Prior Art)



Cascode Current Mirror
FIG. 7B (Prior Art)

